

ABSTRACT

A display apparatus which can display a ultra-high definition picture and high-speed animation is provided.

The input picture signal is connected by signal generation
5 circuit 20 which supplies a desired signal to X driver 30
according to n-gradation approximation picture signal output
from n-gradation approximation calculating circuit 10 to
convert into n-gradation approximation picture signal
approximated to binary gradation in every block and n-gradation
10 approximation calculating circuit 10, Y driver 40, common voltage
generating circuit 50, signal supply circuit 60 and X driver
30, and a plurality of pixel parts 100 connected by X signal
line 31 and Y driver 40 which extends in the Y direction, and
provided to the intersection parts of Y signal line 41 which
15 expands in the X direction.

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